

Fig. 1

**FIRST PREFERRED EMBODIMENT
FREQUENCY SYNTHESIZER APPARATUS**

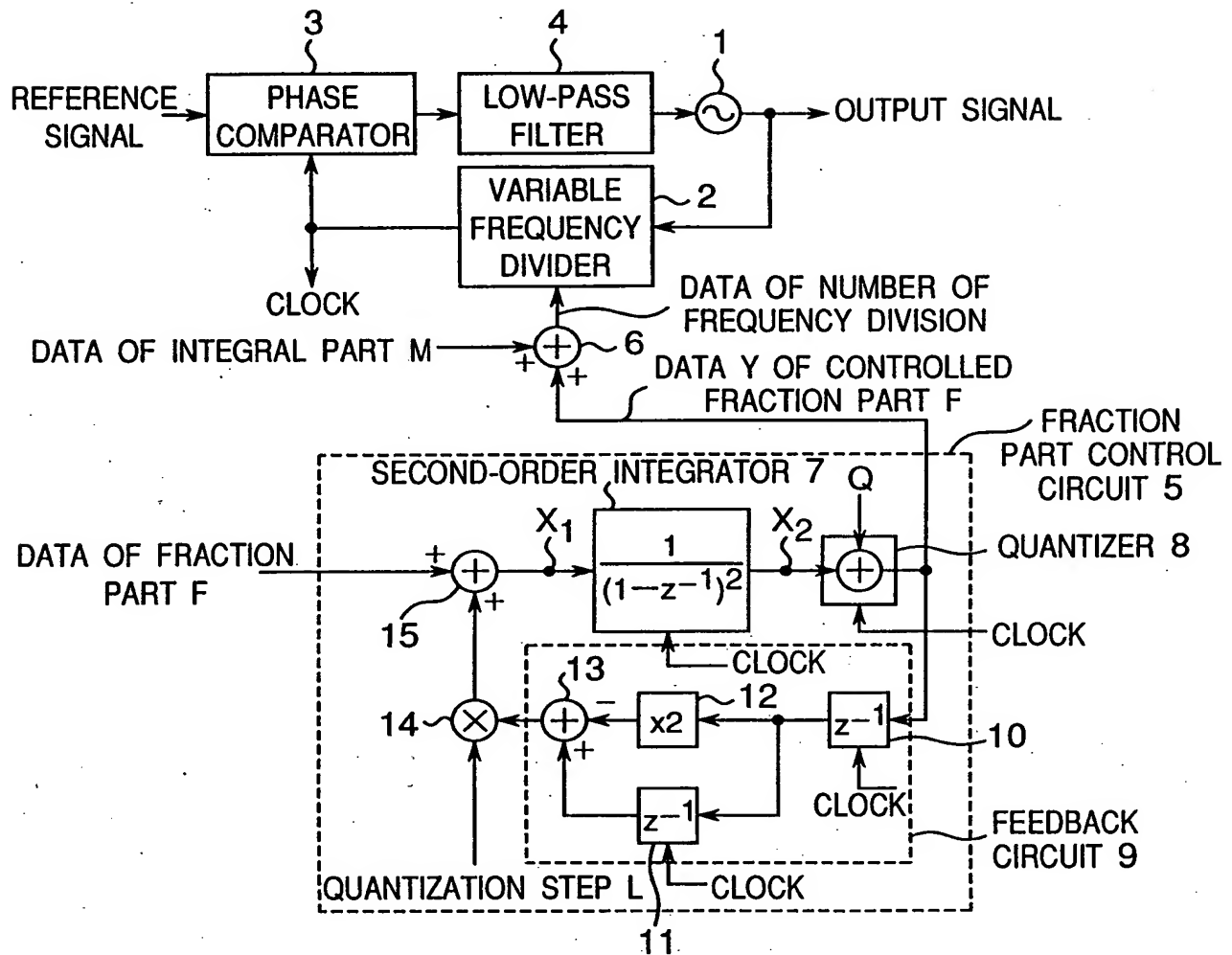


Fig.2

SECOND-ORDER
INTEGRATOR 7

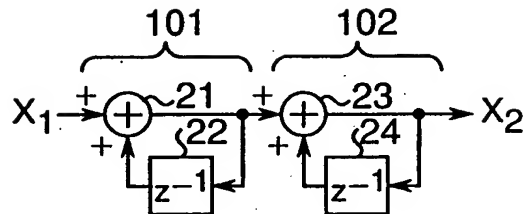


Fig.3

MODIFIED PREFERRED EMBODIMENT
SECOND-ORDER
INTEGRATOR 7a

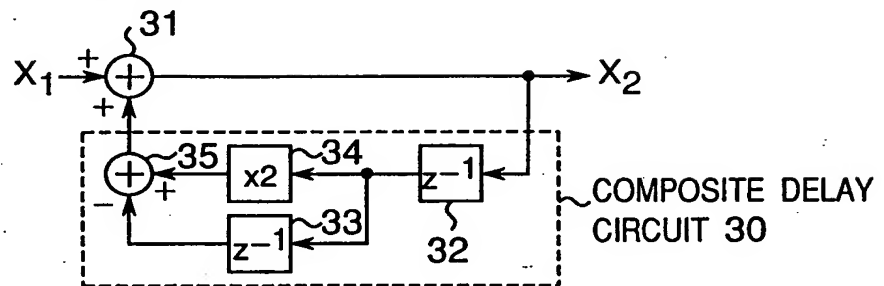


Fig.4

SECOND PREFERRED EMBODIMENT

FRACTION PART CONTROL CIRCUIT 5a

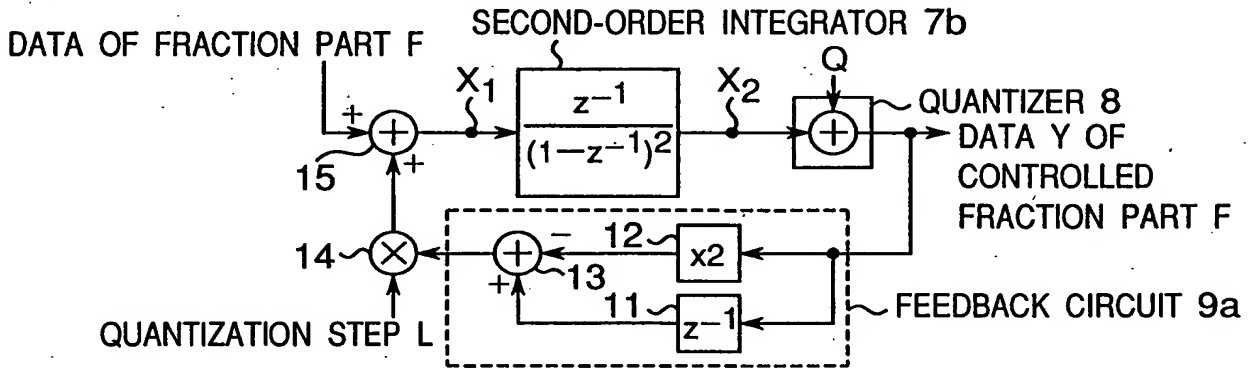


Fig.5

SECOND-ORDER
INTEGRATOR 7b

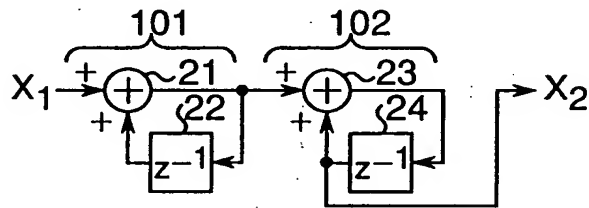


Fig.6

MODIFIED PREFERRED EMBODIMENT
SECOND-ORDER
INTEGRATOR 7c

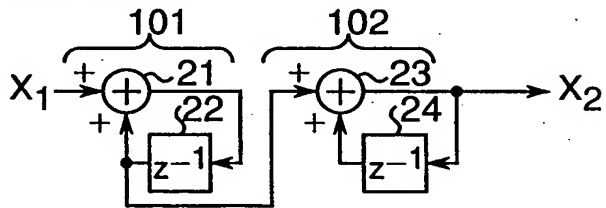


Fig.7

MODIFIED PREFERRED EMBODIMENT
SECOND-ORDER
INTEGRATOR 7d

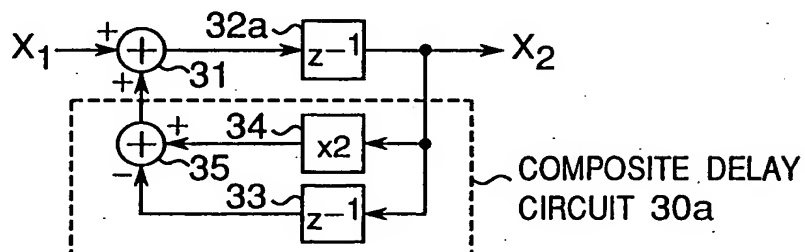


Fig.8

THIRD PREFERRED EMBODIMENT
FRACTION PART CONTROL CIRCUIT 5b

DATA OF FRACTION PART F

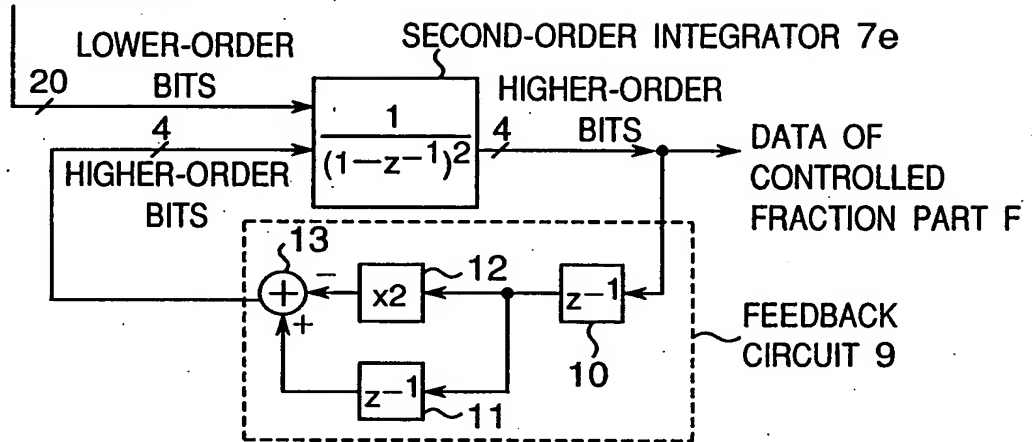


Fig.9

FOURTH PREFERRED EMBODIMENT
FRACTION PART CONTROL CIRCUIT 5c

DATA OF FRACTION PART F

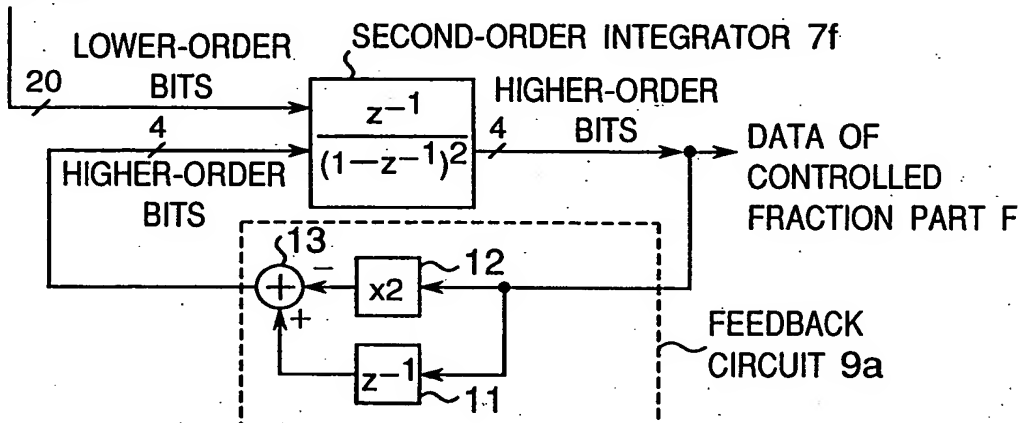


Fig. 10

FIFTH PREFERRED EMBODIMENT
FRACTION PART CONTROL CIRCUIT 5d

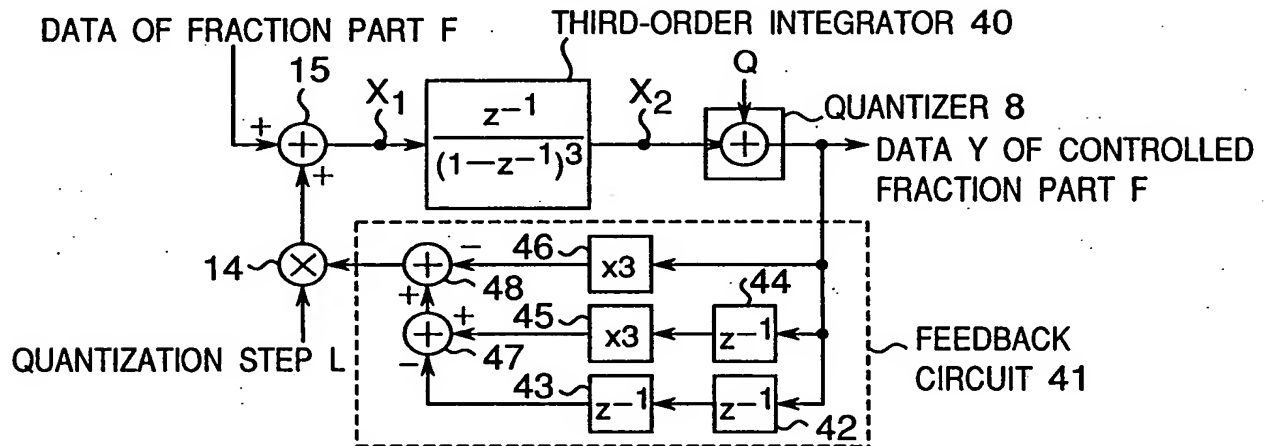


Fig. 11

THIRD-ORDER
INTEGRATOR 40

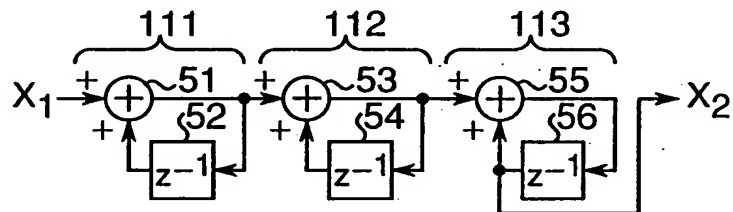
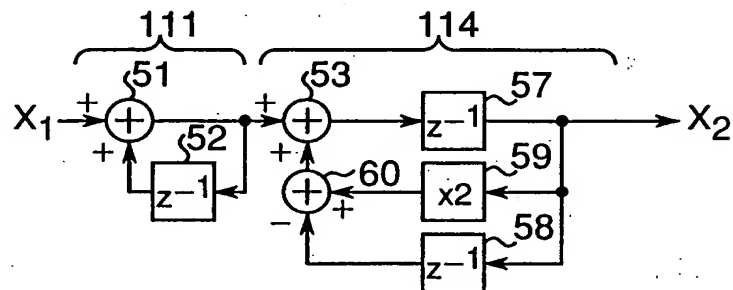


Fig. 12

MODIFIED PREFERRED EMBODIMENT
THIRD-ORDER
INTEGRATOR 40a



MODIFIED PREFERRED EMBODIMENT
THIRD-ORDER
INTEGRATOR 40b

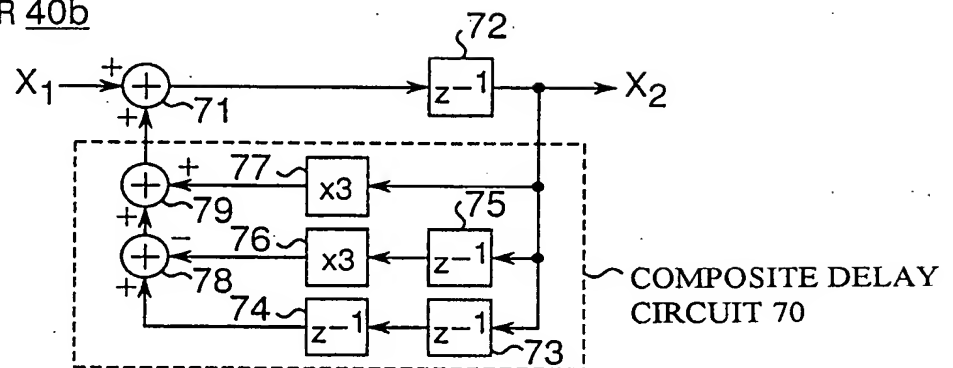


Fig. 14

SIXTH PREFERRED EMBODIMENT
FRACTION PART CONTROL CIRCUIT 5e

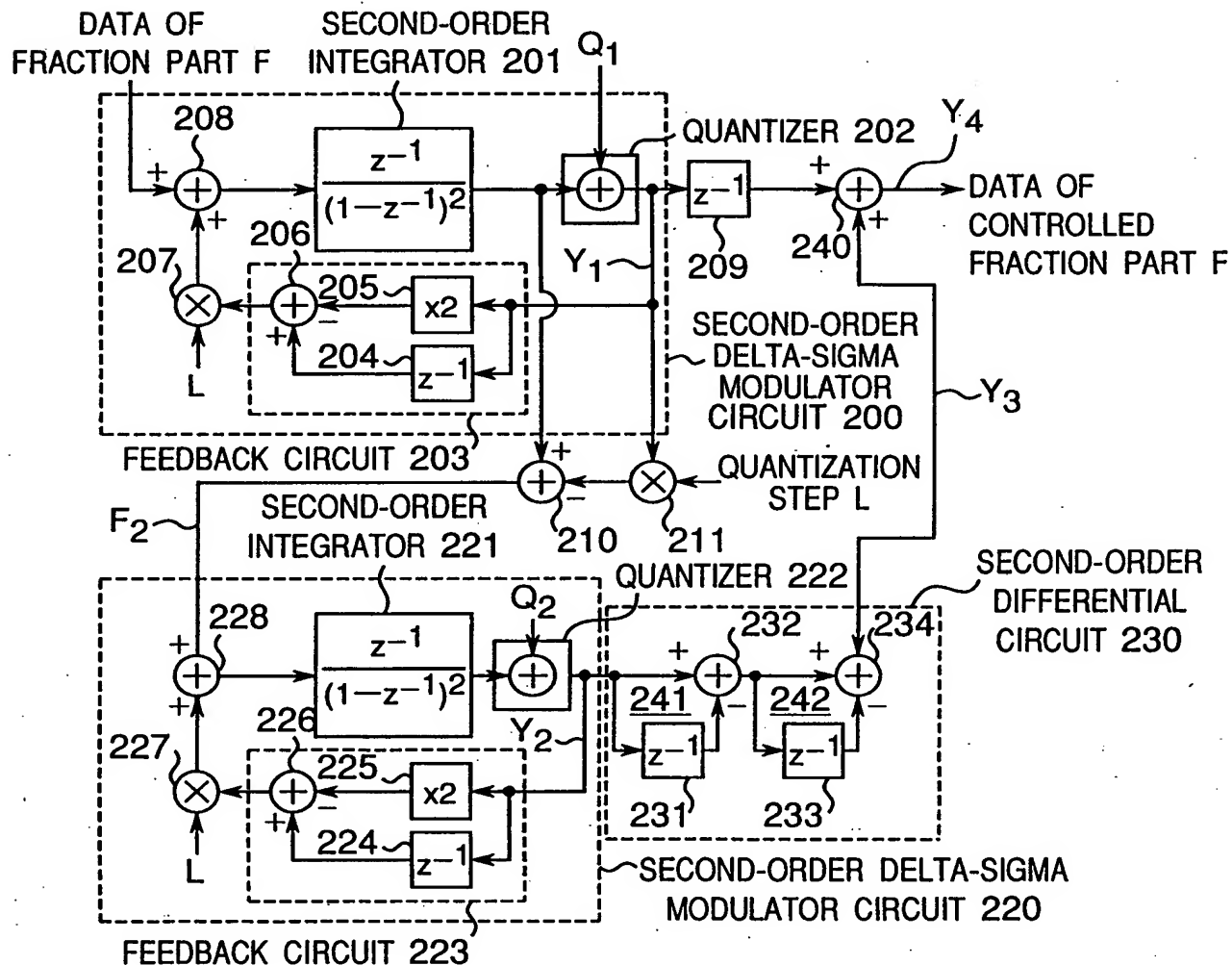


Fig. 15

SEVENTH PREFERRED EMBODIMENT
FREQUENCY SYNTHESIZER APPARATUS

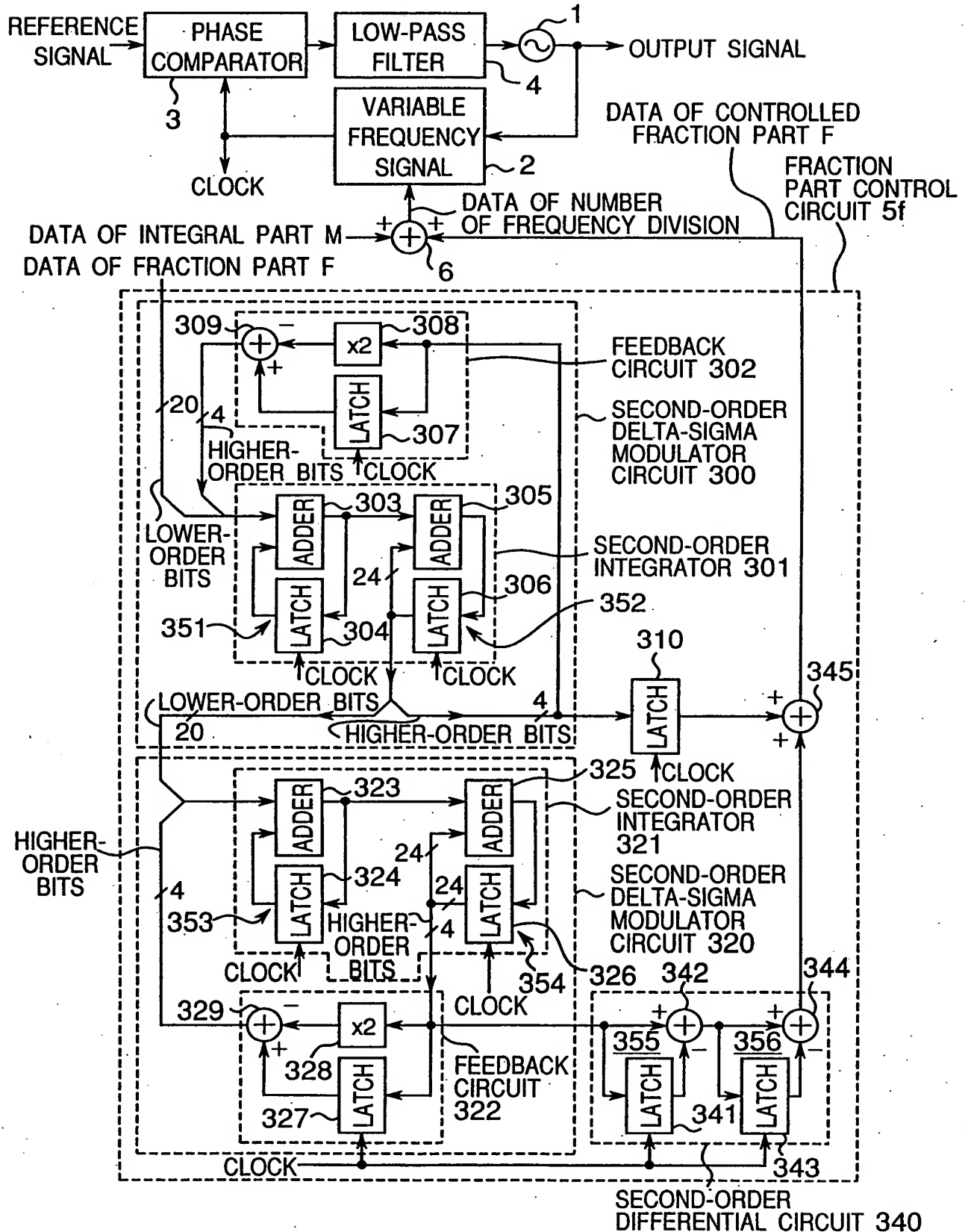


Fig.16

EIGHTH PREFERRED EMBODIMENT
RADIO COMMUNICATION APPARATUS

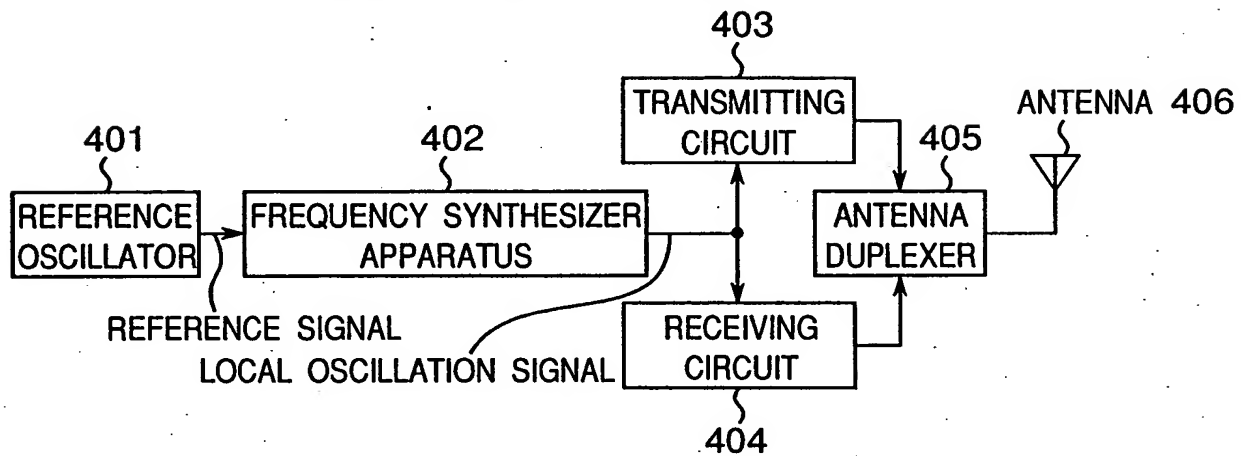


Fig.17

NINTH PREFERRED EMBODIMENT
FREQUENCY MODULATOR APPARATUS

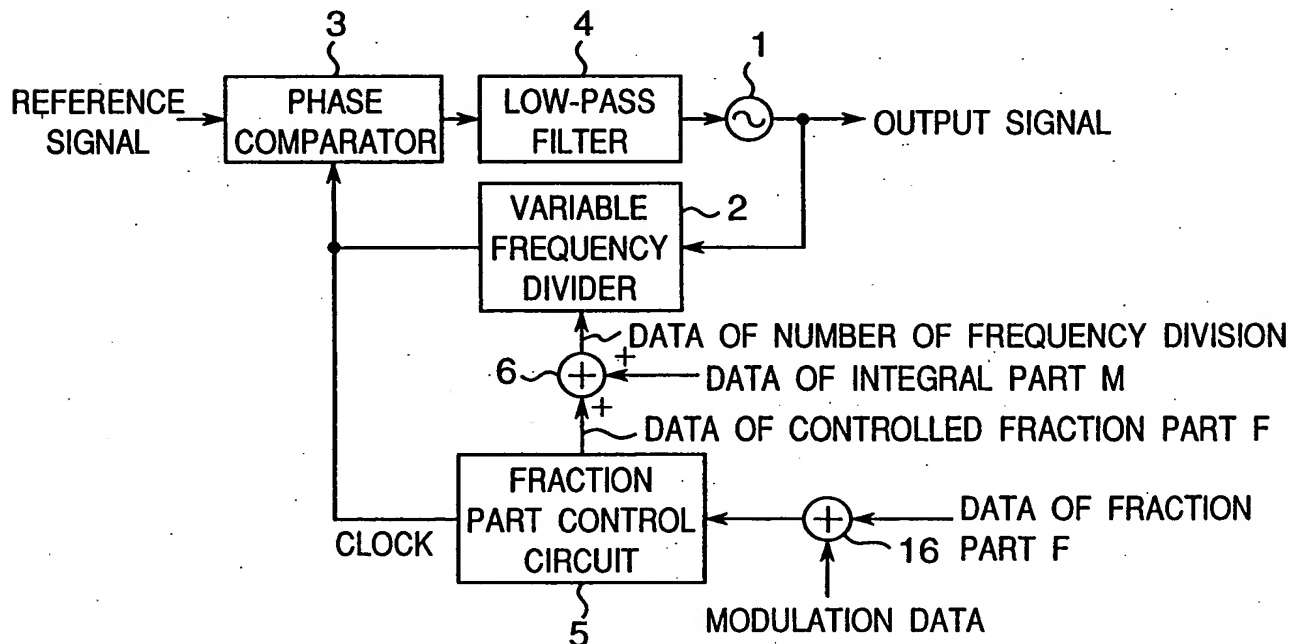


Fig. 18

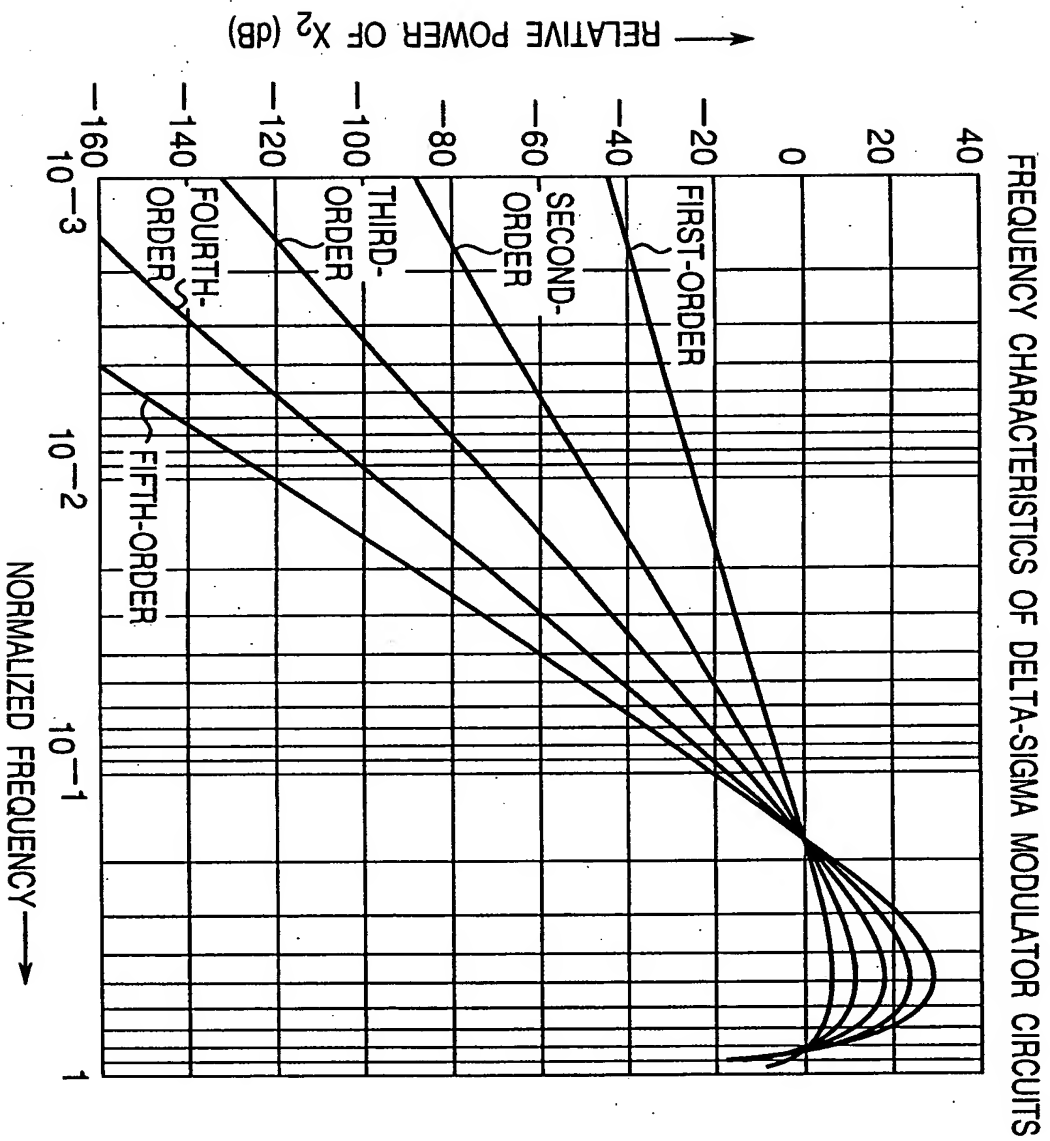


Fig.19 PRIOR ART

FREQUENCY SYNTHESIZER APPARATUS

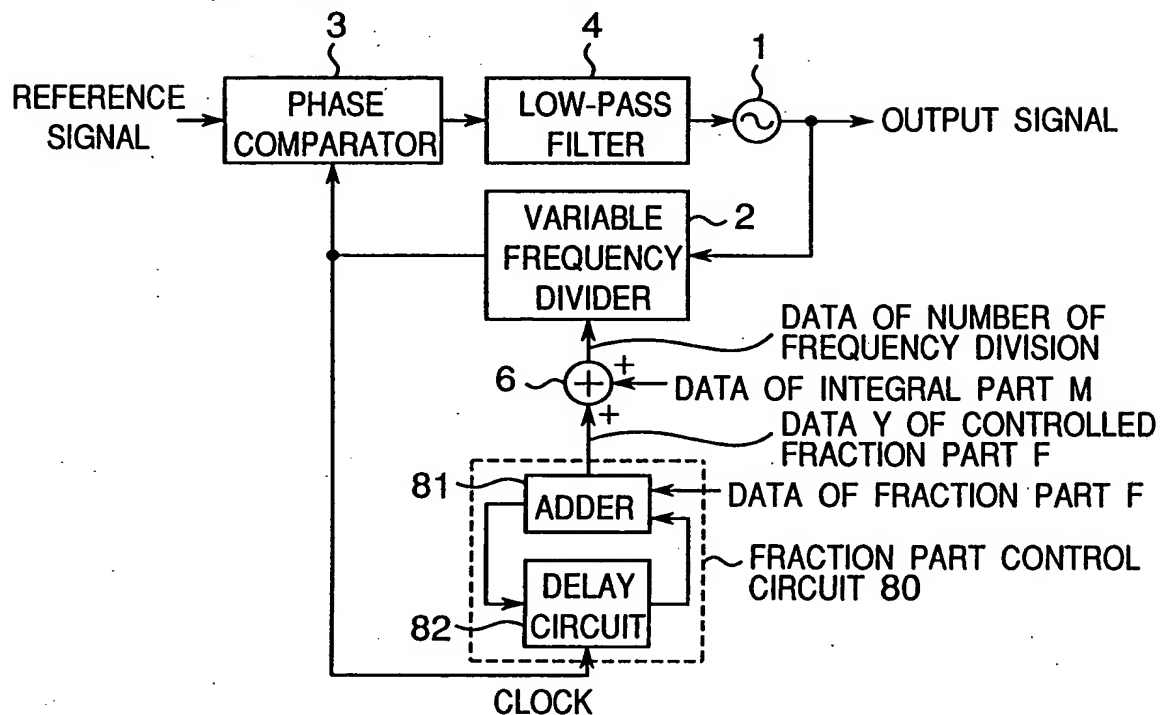


Fig.20 PRIOR ART

FRACTION PART CONTROL CIRCUIT 80

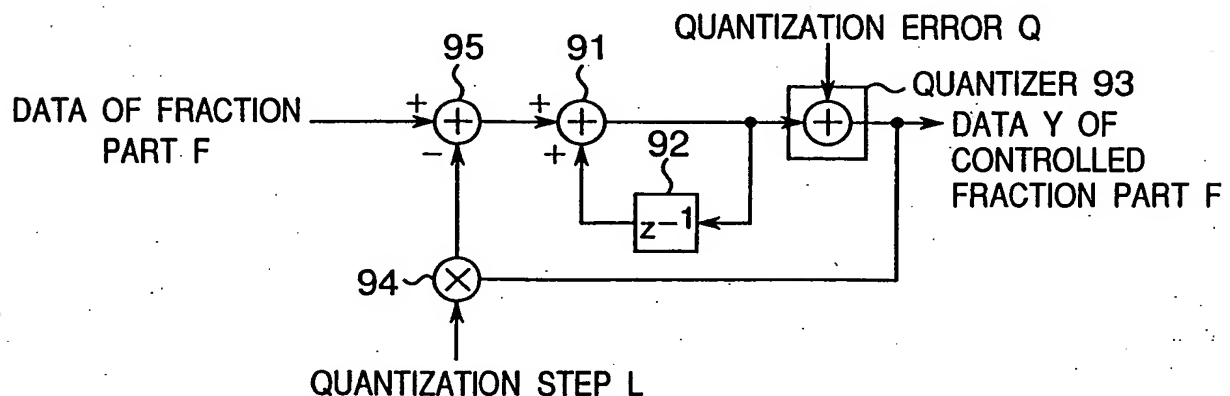


Fig.21 PRIOR ART

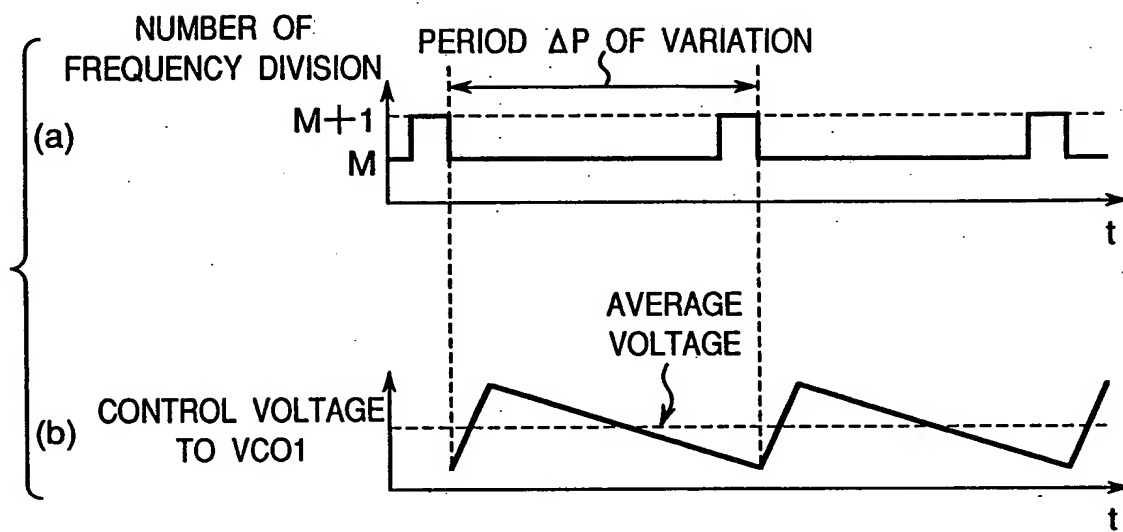


Fig.22 PRIOR ART

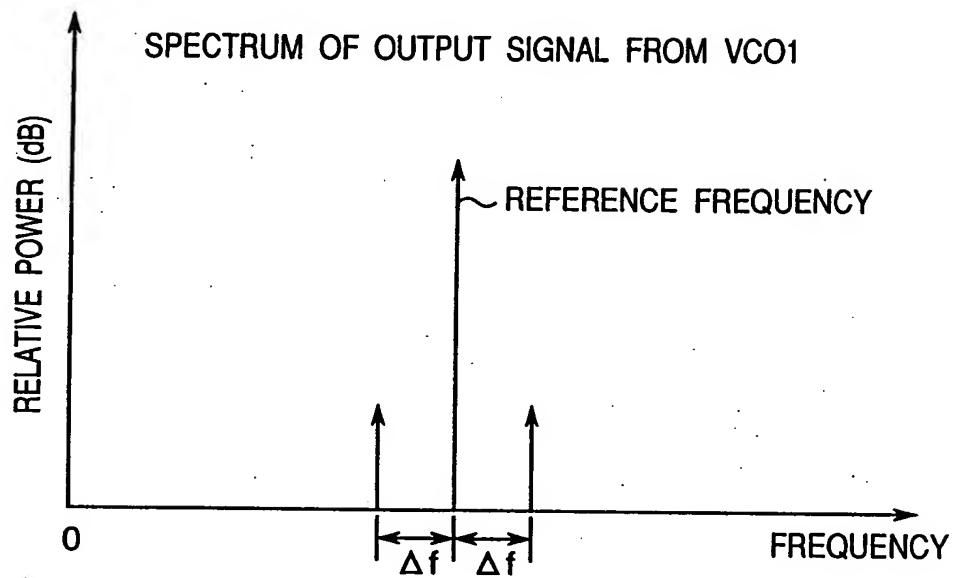


Fig.23 PRIOR ART

